

AMENDMENT TO SPECIFICATION

In the Specification

A marked-up copy of the changes to selected paragraph(s) is provided below. Please enter these changes to the specification in the record.

Please replace the paragraph beginning on page 7, line 4 with the following amended paragraph:

This invention separately provides a method for driving a PDP for arranging a plurality of scan electrodes and sustain electrodes in parallel for each display line, and arranging a plurality of address electrodes to be crossed with the scan electrodes and the sustain electrodes, by (a) charging, to a third voltage, a capacitor having a first end selectively coupled to a first voltage and a second voltage, ~~to a third voltage~~; (b) supplying the first voltage to the first end of the capacitor, and turning on a rising ramp switch for supplying a constant current to the scan electrode to make the potential of the scan electrode rise to the third voltage from the first voltage in a ramp waveform, the rising ramp switch being coupled between a second end of the capacitor and the scan electrode; (c) turning off the rising ramp switch, and supplying the second voltage to the first end of the capacitor to control the potential of the scan electrode to a fourth voltage; and (d) turning on a main path switch for supplying the constant current to the scan electrode to make the potential of the scan electrode gradually fall, the main path switch being coupled between the second voltage and the scan electrode.

Please replace the paragraph beginning on page 9, line 19 with the following amended paragraph:

The charge and discharge unit 720 comprises switches Yr and Yf, diodes D1 and D2, a capacitor C0, and an inductor L1. The voltage of $V_s/2$ is charged to the capacitor C0, and serial resonance between the inductor L1 and the panel capacitor ~~[[Cp]]~~ C0 is used to make the potential of a scan electrode rise to the

voltage of V_s or to make it fall to the voltage of V_g . The switch Y_r is turned on to make the potential rise to the voltage of V_s , and the switch Y_f is turned on to make the potential fall to the voltage of V_g . The diodes D_1 and D_2 set charge and discharge current paths.

Please replace the paragraph beginning on page 10, line 17 with the following amended paragraph:

~~The main path switch~~ Y_p & Y_{fr} function[[s]] as a main path switch for preventing a high voltage for supplying the reset waveform provided before addressing from being supplied to the sustain waveform former operable with a low voltage in the conventional PDP driving device. The main path switch Y_p & Y_{fr} also functions as a falling ramp switch for generating a falling ramp waveform which is a portion of the reset waveform. Therefore, the main path switch Y_p & Y_{fr} that is a MOSFET connects a capacitor C_2 between a gate and a drain to form ramp waveforms, and it includes a body diode.

Please replace the paragraph beginning on page 11, line 12 with the following amended paragraph:

A switch Y_{sp} flows the current to the sustain unit from the scan electrode.